

Serial No. 10/681,577 Atty. Docket No. IMPJ-0004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Christopher Diorio

SERIAL NO.:

10/681,577

FILING DATE:

10/07/03

TITLE:

USE OF ANALOG-VALUED FLOATING-GATE TRANSISTORS TO

MATCH THE ELECTRICAL CHARACTERISTICS OF

INTERLEAVED AND PIPELINED CIRCUITS

EXAMINER:

Nguyen, Hail

ART UNIT:

2816

CERTIFICATE OF MAILING

I horeby certify that this paper is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria VA 22313-1450, on the date printed below:

Name

Wuth Rodriquez

Date:

Mail Stop Amendment **Commissioner for Patents** P.O. Box 1450 Alexandria, VA 22313-1450

AMENDMENT AFTER NOTICE OF APPEAL BUT BEFORE APPEAL BRIEF **PURSUANT TO 37 C.F.R. § 41.33**

Sir:

Please amend the subject application as indicated.

Amendments to the Claims are reflected in the Listing of Claims beginning on page 2.

Remarks begin on page 12.